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3-22-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

#3

In the Application of:

Guven et al.

Atty. Docket: TI-32148

Serial No.: 09/750,264

Art Unit: TBD

Filed: December 29, 2000

Examiner: TBD

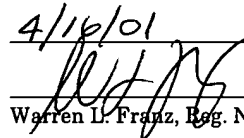
For: MODEM RELAY PROTOCOL  
REDUNDANCY FOR RELIABLE  
LOW SPEED MODEM  
COMMUNICATIONS OVER IP  
NETWORKS WITH SUBSTANTIAL  
PACKET LOSS

Date: April 16, 2001

Assistant Commissioner for  
Patents  
Washington, D.C. 20231

**CERTIFICATE OF MAILING 37 CFR §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231 on the date indicated below.

4/16/01  


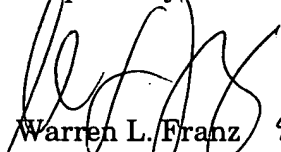
Warren L. Franz, Reg. No. 28,716

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Sir:

Please find enclosed eight (8) sheets of substitute/formal drawings for the subject application as required by the Notice to File Corrected Application Papers mailed February 15, 2001, a copy of which is also enclosed.

Respectfully submitted,

  
Warren L. Franz 4/16/01  
Attorney for Applicant(s)  
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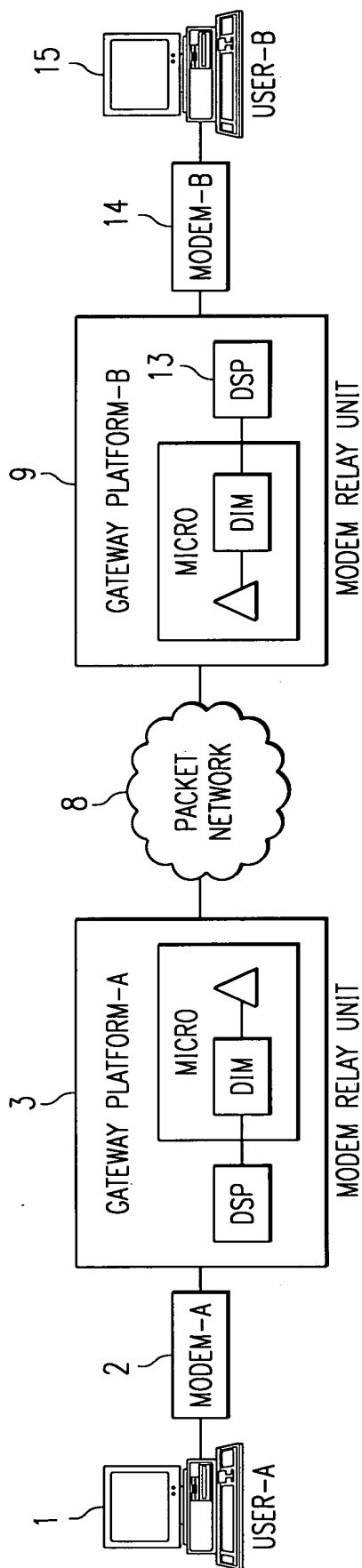


FIG. 1

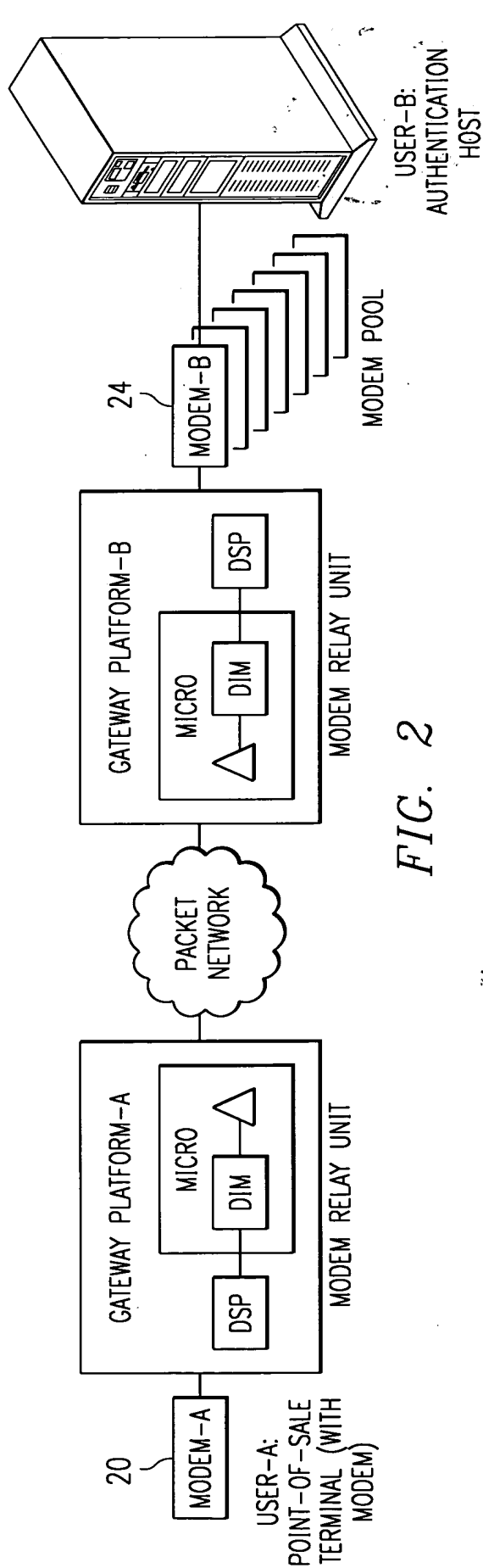


FIG. 2

The diagram illustrates the timing of various modem signals during a call. The signals are represented as horizontal bars indicating their duration. The sequence of events is as follows:

- V.25 ANSWER TONE (V.25bis):** The initial signal, labeled "V.25 ANSWER TONE".
- V.22bis @ 1200 bps (SB1 @ 1200 bps):** A signal that starts after the answer tone and continues through the V.22 and V.21 modem stages.
- V.22 MODEM (SB1 @ 2400 bps):** A signal that starts after the V.22bis signal and continues through the V.21 modem stage.
- V.21 MODEM (DATA):** A signal that starts after the V.22bis signal and continues through the V.32bis/V.32 stage.
- V.32bis -OR- V.32 (AC, DATA):** A signal that starts after the V.22bis signal and continues through the V.21 flags stage.
- V.21 FLAGS:** A signal that starts after the V.22bis signal and continues through the V.32bis/V.32 stage.
- SWITCH TO MODEM RELAY:** A point in time indicated by an arrow, occurring after the V.25 answer tone and before the V.22bis signal.
- High Speed Modem Call - Will Be Dropped:** A note indicating that a high-speed modem call will be dropped.

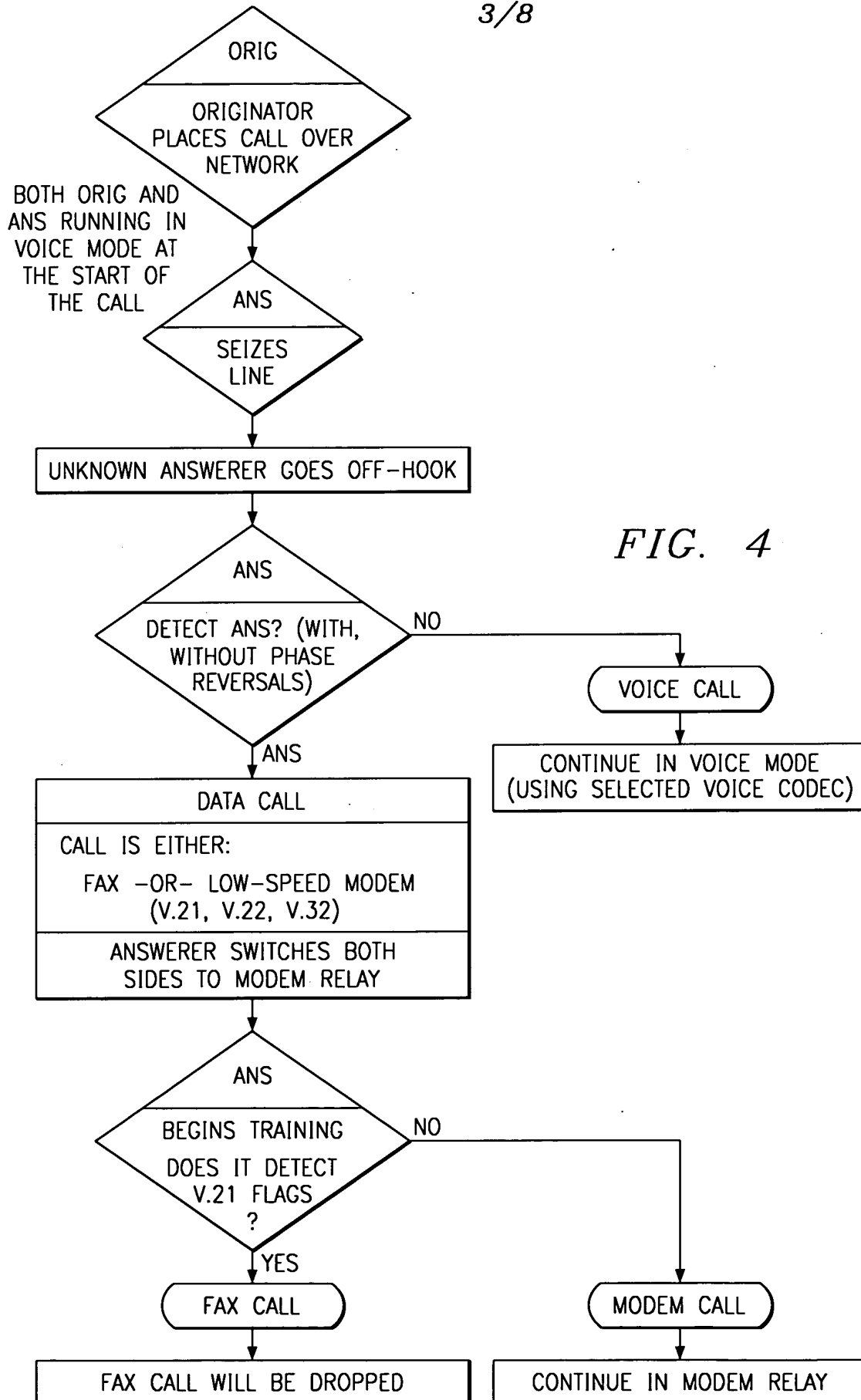
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The diagram illustrates the architecture of a GDM (Global Data Module) system. It is organized into several functional blocks and their interconnections:

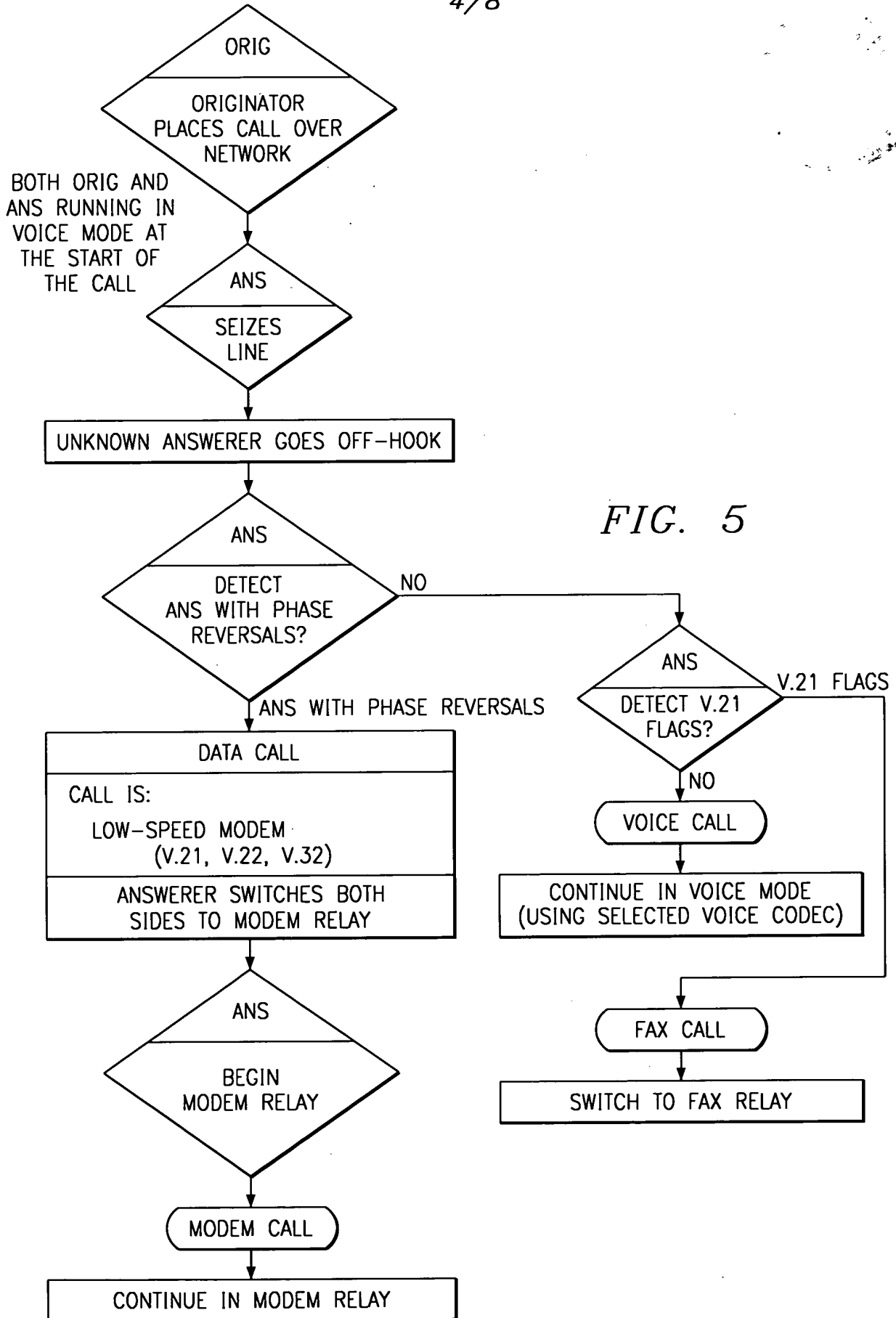
- PCM DRIVER**: The primary input/output interface at the bottom, connected to the **PCM INTERFACE UNIT**.
- PCM INTERFACE UNIT**: Acts as a bridge between the PCM Driver and the internal processing units.
- TX Path (Transmit)**:
  - Signal from PCM Interface Unit passes through **ECHO CANCELLER UNIT** and **TX GAIN**.
  - CALLER ID DETECT** and **VOICE ACTIVITY DETECTION UNIT** are connected to the TX path.
  - VOICE ACTIVITY DETECTION UNIT** also provides input to the **VOICE CODEC UNIT**.
- RX Path (Receive)**:
  - Signal from PCM Interface Unit passes through **RX GAIN**.
  - CALLER ID GENERATOR** and **VOICE CODEC UNIT** provide input to the RX path.
- VOICE CODEC UNIT**: A central unit supporting various codecs: G.711, G.726, G.727, G.728, G.729B, G.729AB, and G.732.1A.
- PACKETIZED VOICE PROTOCOL UNIT**: Receives data from the TX path and sends it to the **HPI**.
- VOICE PLAYOUT UNIT**: Receives data from the **PACKETIZED VOICE PROTOCOL UNIT** and sends it back to the **PCM INTERFACE UNIT**.
- CLEAR TANDEM UNIT**: Two units are shown, one in the TX path and one in the RX path, likely for network interconnection.
- Software and Message Processing**:
  - ALL GDM UNITS** and **SOFTWARE INTEGRATION UNIT** are connected to the **PACKETIZED VOICE PROTOCOL UNIT**.
  - MESSAGE PROCESSOR UNIT** is connected to the **SOFTWARE INTEGRATION UNIT**.
- HPI (Host Processor Interface)**: The final output interface at the top, connected to the **PACKETIZED VOICE PROTOCOL UNIT**.

FIG. 6

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FIG. 7

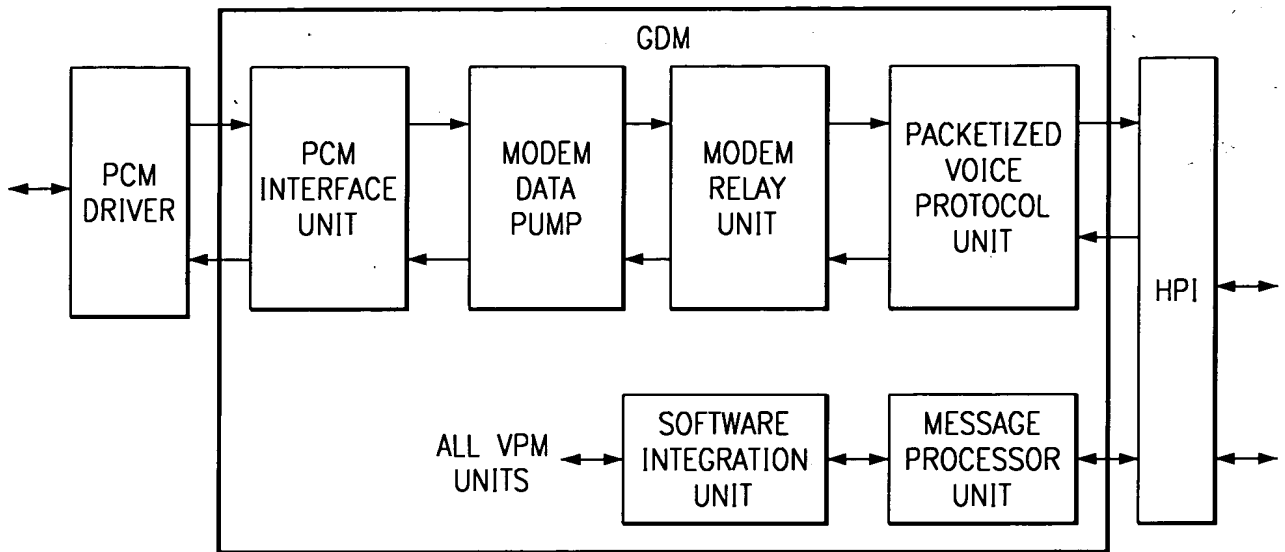
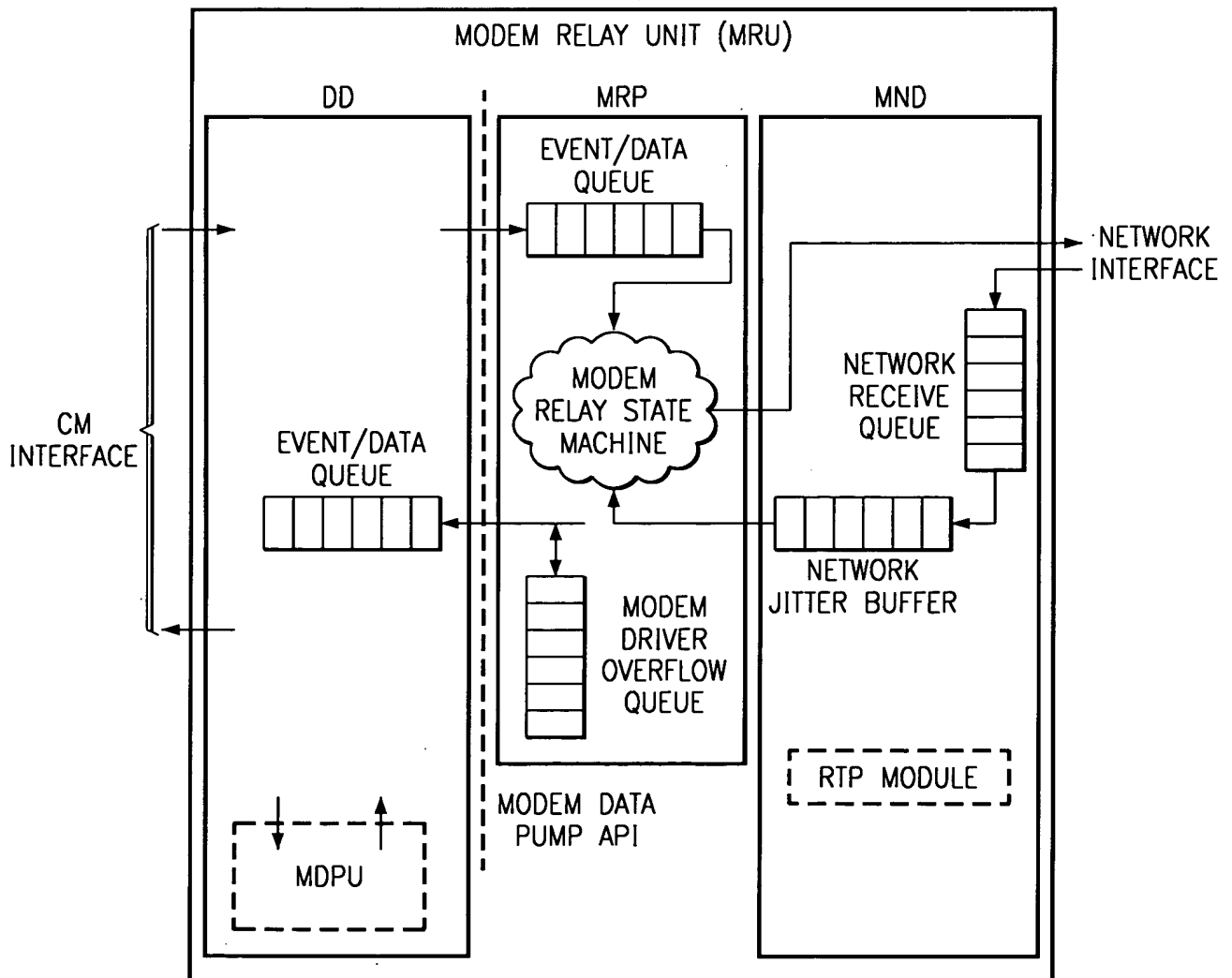


FIG. 8



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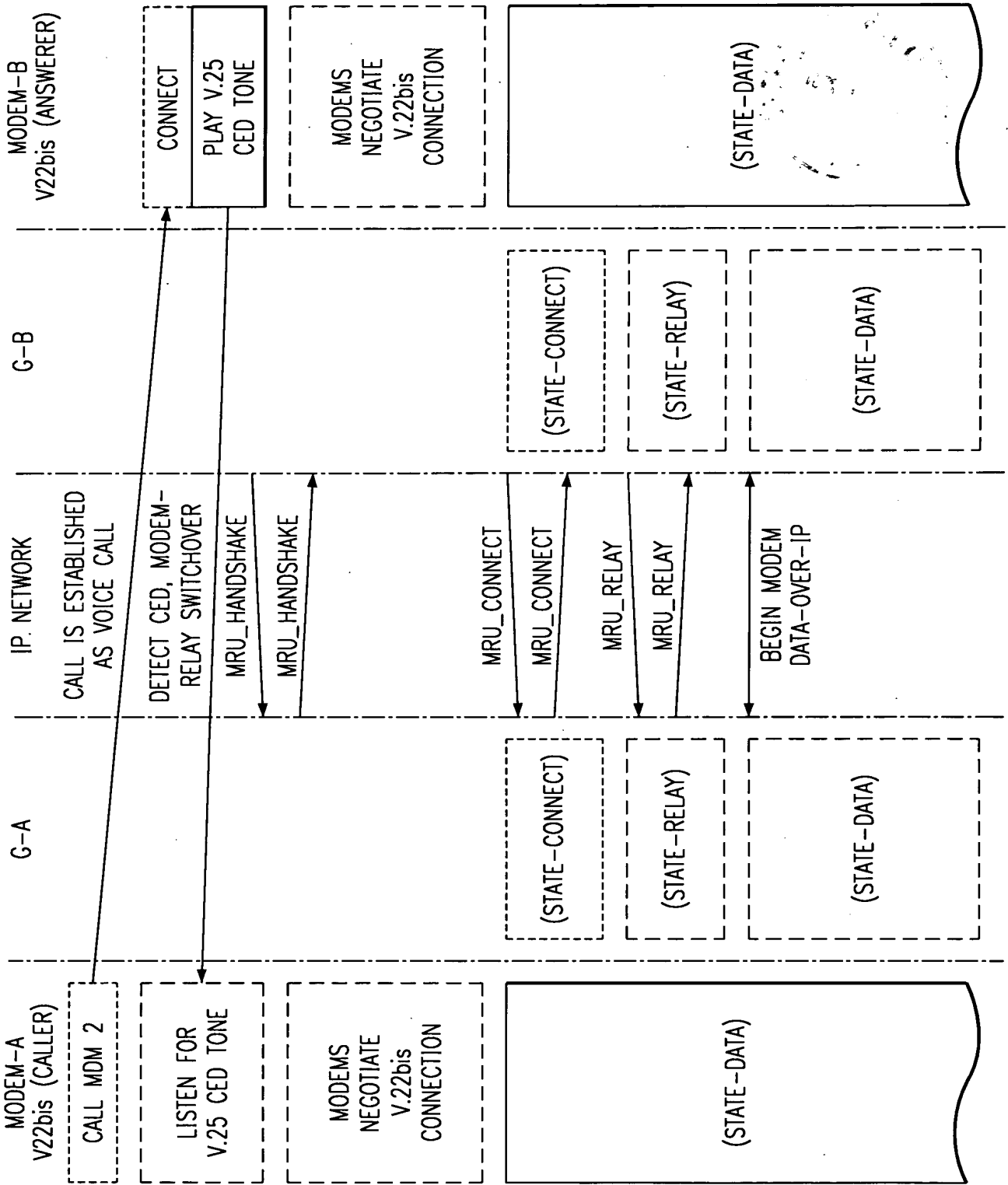


FIG. 9

FIG. 10

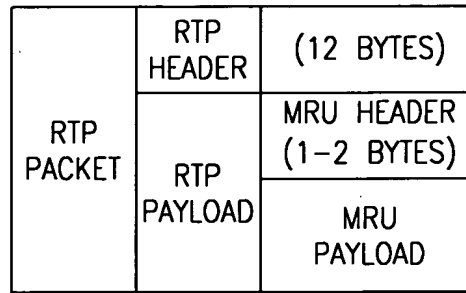
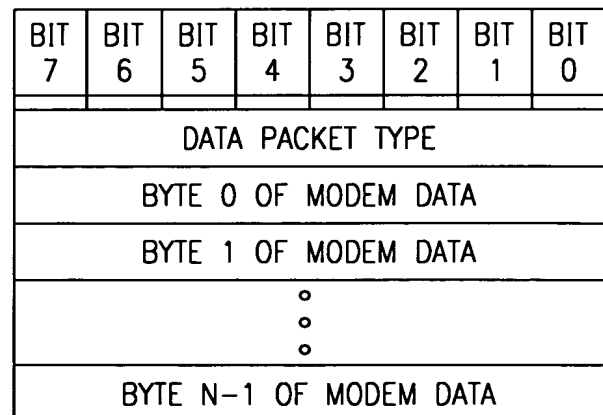
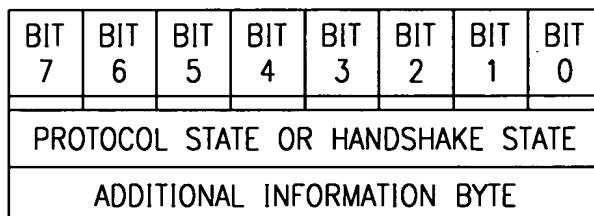
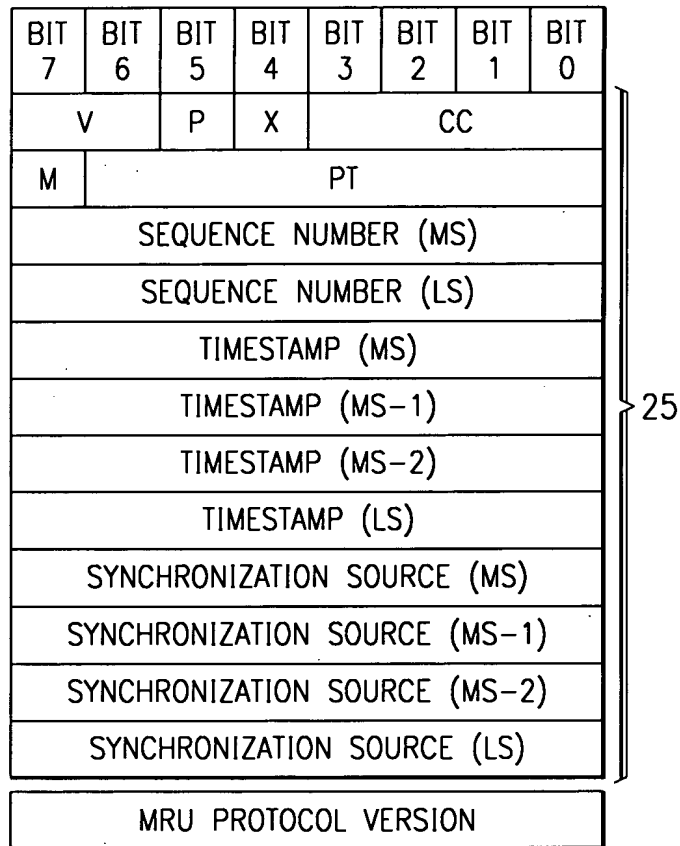


FIG. 11





MODEM RELAY PROTOCOL STATE PACKETS

PROTOCOL STATE	DESCRIPTION
OFFLINE	IDLE STATE
CARRIER LOSS	SILENCE ON THE ANALOG LINE, CARRIER LOSS
HANDSHAKE	MODEM DATA PUMPS WILL BEGIN HANDSHAKING PROCESS
CONNECT	LOCAL HANDSHAKE SESSION IS PASSED AND READY FOR MODEM RELAY
RELAY	MODEM RELAY TAKES PLACE

MODEM HANDSHAKE STATE PACKETS

HANDSHAKE STATE	DESCRIPTION
V25	V.25 ANSWER TONE IS DETECTED ON THE LOCAL ANALOG LINE
V25PR	V.25 ANSWER TONE WITH PHASE REVERSALS IS DETECTED ON THE LOCAL ANALOG LINE
V21	V.21 B1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
USB1	V.22 USB1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
S1	V.22bis S1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
SB1_1200	V.22bis SB1 SIGNAL @ 1200 IS DETECTED ON THE LOCAL ANALOG LINE
SB1_2400	V.22bis SB1 SIGNAL @ 2400 IS DETECTED ON THE LOCAL ANALOG LINE

MODEM DATA PACKETS

DATA TYPE	DESCRIPTION
V21	V.21 DATA @ 300 bps
V22	V.22 DATA @ 1200 bps
V22BIS	V.22bis DATA @ 2400bps

FIG. 12